# Notes:

1. Allow clearance for finger squeeze access from the two sides shown.

2. No tracks or vias in the shaded "KEEP OUT" AREA.

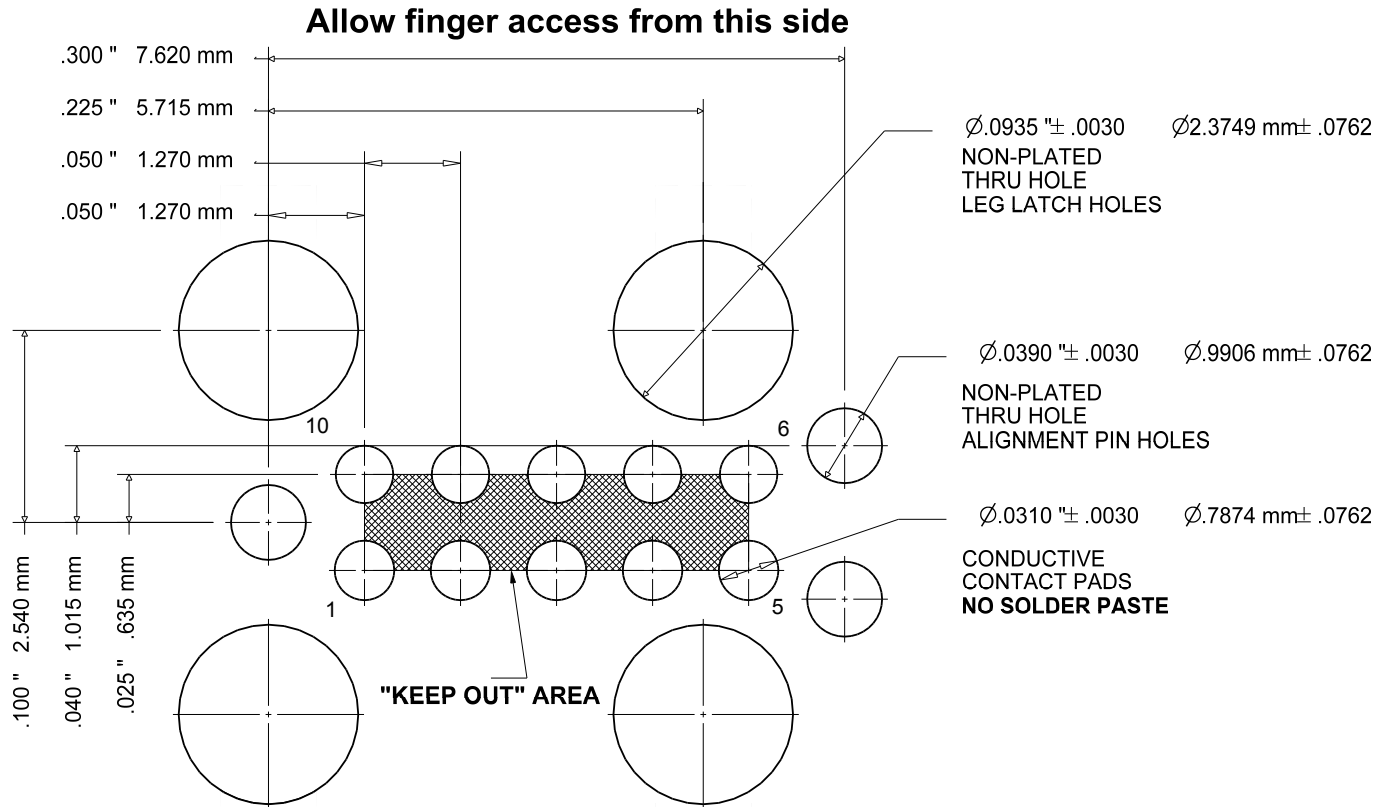3. No other track or signal within .02" of any contact pad.

4. Do not allow solder paste on the contact pads.It is essential to eliminate any hole in the solder paste-mask (solder stencil) layer. If this is not possible make the contact pads thru-hole with a finished hole size of .008" or less.

5. Leg and alignment holes may be plated and used as vias but ensure minimum finshed hole size meets specified tolerances.
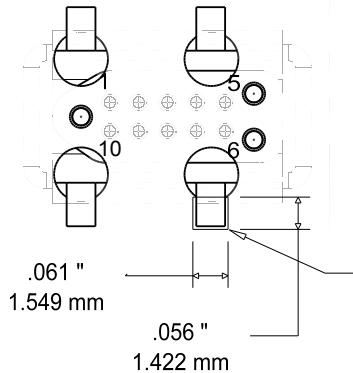
6. To prevent purchasing confusion, please specify DNL in your BOM.

**Allow finger access from this side**

.300 "   7.620 mm
.225 "   5.715 mm
.050 "   1.270 mm
.050 "   1.270 mm

.100 "   2.540 mm
.040 "   1.015 mm
.025 "   .635 mm

10
6
1
5

⌀.0935 "± .0030      ⌀2.3749 mm± .0762
NON-PLATED
THRU HOLE
LEG LATCH HOLES

⌀.0390 "± .0030      ⌀.9906 mm± .0762
NON-PLATED
THRU HOLE
ALIGNMENT PIN HOLES

⌀.0310 "± .0030      ⌀.7874 mm± .0762
CONDUCTIVE
CONTACT PADS
**NO SOLDER PASTE**

"KEEP OUT" AREA

**Allow finger access from this side**

PCB BOTTOM VIEW

5
10   6

.061 "
1.549 mm

.056 "
1.422 mm

ALLOW A .061 x .056
SQUARE AREA OF
CLEARANCE FOR
EACH FOOT



| SIZE | FSCM NO. | DWG NO. | **FOOTPRINT FOR** | REV |
|------|----------|---------|-------------------|-----|
| **A** | | | **TC2050-IDC-FP** | **A** |
| SCALE  7:1 | www.TAG-CONNECT.com | | SHEET  1 Of 3 | |

# TC2050-IDC-430 PLUG-OF-NAILS(TM) CABLE

| ZONE | A | | TC2050-IDC-430 | 11/26/10 | MS |

**NOTE:**
**Dimensions subject to change**

**For use with TI MSP-FET and compatible MSP430 JTAG debuggers**

8 "
[203.2 mm]

RED WIRE

.338 "
[8.595 mm]
.800 "
[20.320 mm]
.08 "
[2.032 mm]

.131 "
[3.327 mm]
.050 "
[1.270 mm]

.250 "
[6.350 mm]
.160 "
[4.060 mm]

.4858 "
[12.34 mm]
.1807 "
[4.59 mm]
.400 "
[10.150 mm]

.300 "
[7.620 mm]
.2935 "
[7.4549 mm]

Tag-Connect inserted
into PCB for
programming / debugging
PCB

.088 "
[2.2352 mm]
.050 "
[1.27 mm]
.2935 "
[7.4559 mm]

**A**

RED WIRE
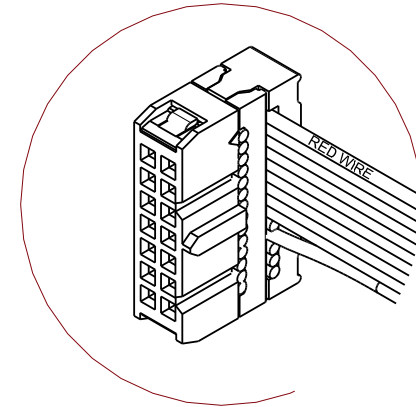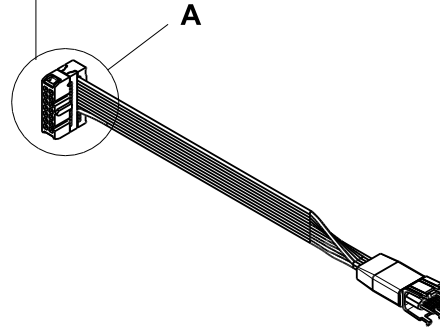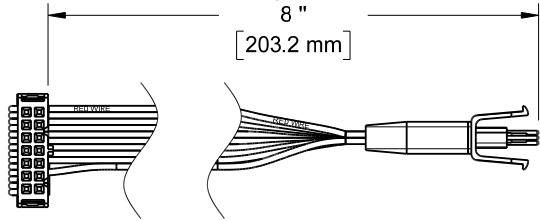
Detail A

## END VIEW
## 2x7 0.1" PITCH
## IDC CONNECTOR

1          13

2          14
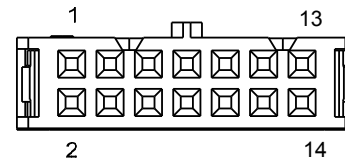
## END VIEW
## 10 PIN
## TAG-CONNECT
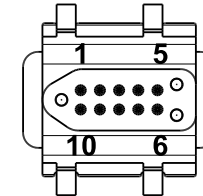
| 1 | 5 |
| 10 | 6 |

| 14 pin IDC | 10 pin TAG | 14 pin IDC | 10 pin TAG |
|---|---|---|---|
| 1 | 1 | 6 | 6 |
| 2 | 2 | 7 | 7 |
| 3 | 3 | 8 | 8 |
| 4 | 4 | 9 | 9 |
| 5 | 5 | 11 | 10 |

**NOTE:**
**Pins 10,12,13 & 14 are not used**

| SIZE | FSCM NO. | DWG NO. | | REV |
|---|---|---|---|---|
| A | | TC2050-IDC-430 | | A |

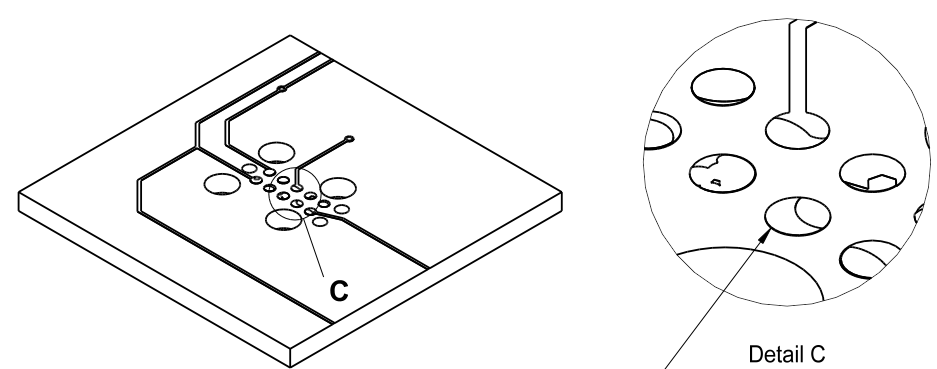| SCALE | www.TAG-CONNECT.com | SHEET 2 Of 3 |
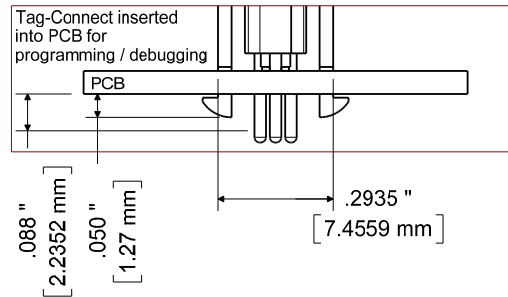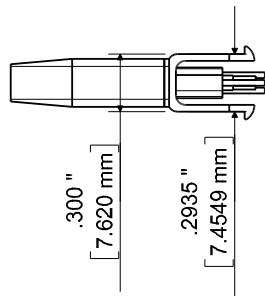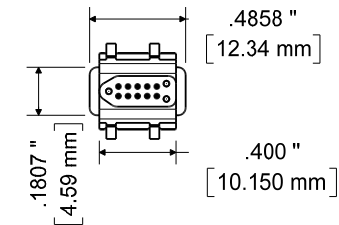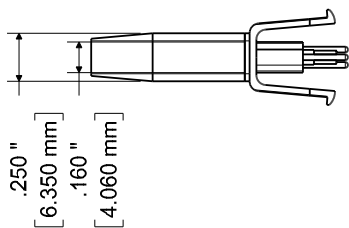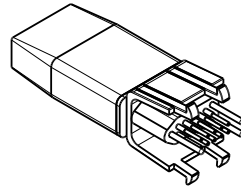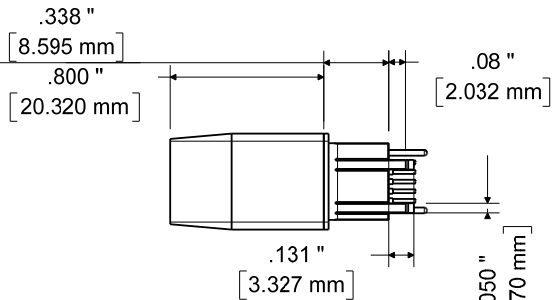
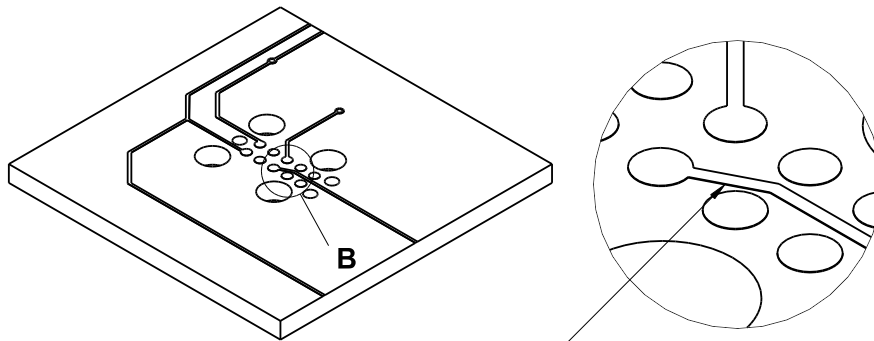# Layout Problems to Avoid
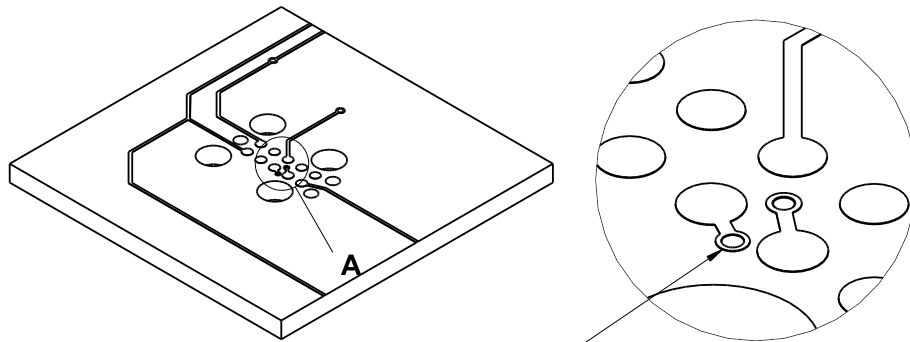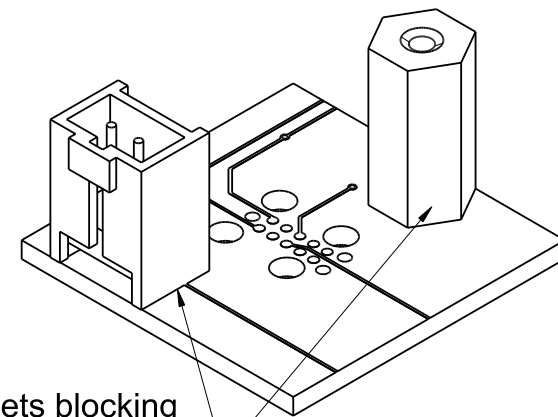


Detail A

Vias between
contact pads

Detail C

Do not deposit solder paste on
contact pads. View Solder Paste
Mask (Solder Stencil) layer in
pad-stack and eliminate any hole.

Detail B

Traces less than .02"
away from contact pad
or in "KEEP OUT" area

Large componets blocking
finger access to the Tag-
Connect leg clips