

Super Low Distortion Multilayer Ceramic Capacitors

UMK105SD561KV-F



■ Features

- Item Summary
560pF \pm 10%, 50V,
Standard, 0402/1005 (EIA/JIS)
- Lifecycle Stage
Mass Production
- Standard packaging quantity (minimum)
Taping paper 10000pcs

■ Products characteristics table

Capacitance	560 pF \pm 10 %
Case Size (EIA/JIS)	0402/1005
Rated Voltage	50 V
tan δ (max)	0.1 %
Operating Temp. Range (EIA)	-55 to +125 °C
Operating Temp. Range (JIS)	-55 to +125 °C
High Temperature Loading (% Rated Voltage)	200 %
Insulation Resistance (min)	10 G Ω
RoHS2 Compliance (10 subst.)	Yes
REACH Compliance (173 subst.)	Yes
Halogen Free	Yes
Soldering	Reflow

■ External Dimensions

Dimension L	1.0 \pm 0.05 mm
Dimension W	0.5 \pm 0.05 mm
Dimension T	0.5 \pm 0.05 mm
Dimension e	0.25 \pm 0.10 mm

2017.04.30

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
 TAIYO YUDEN reserves the right to make change to the Date at any time without notice.
 Before making final selection, please check product specification.

Multi Layer Ceramic Capacitor **TAIYO YUDEN**

-Electrical Characteristics Data-

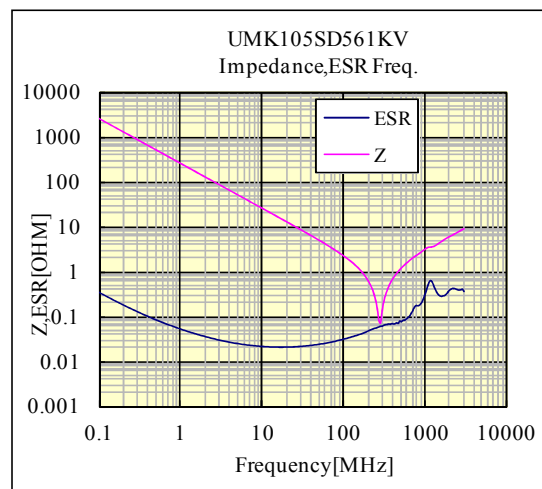
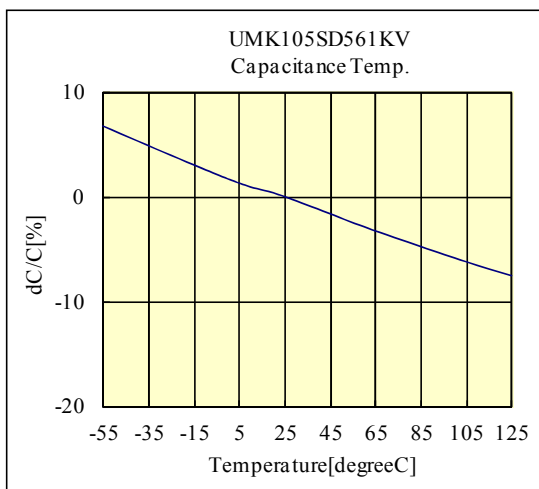
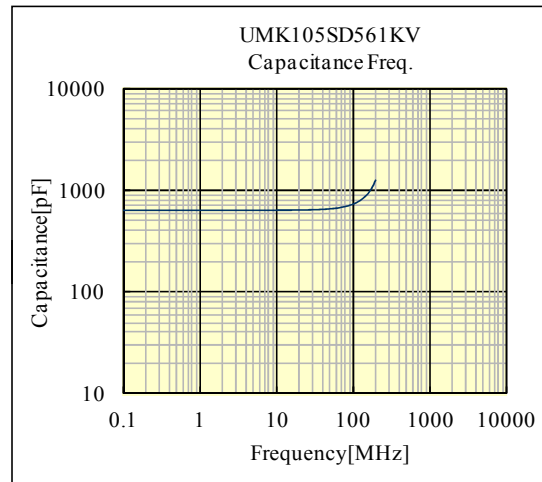
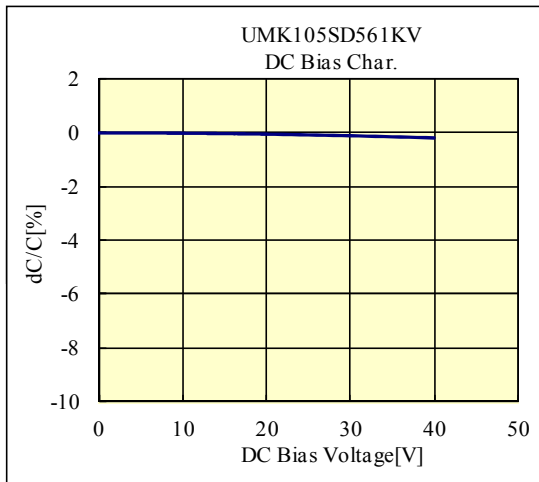
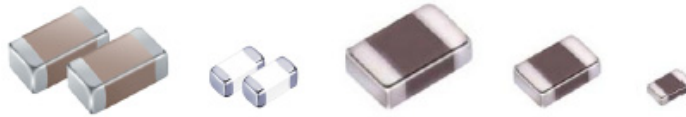
16CJA02

RoHS compliance product

○UMK105SD561KV

50V 1005 560pF +/- 10% t=0.55mmMAX

50V 0402 560pF +/- 10% t=0.022inchMAX



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