

DRAWING NO.	TITLE	DATE	APPROVED	CHECKED	DRAWN



- 4) Before pressing terminals into board, position (rotate) terminals to maximize the clearance between the widest part of the terminal and the nearest adjacent conductor
 - 3) In any plug contact area on either side of plugboard, use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry and using them could cause shorting.
 - 2) Where tin coated circuitry exists, a small percentage of the holes may have solder blockage. This is usually a light "skin" easily penetrated by component leads. In some cases a soldering iron may be required.
 - 1) Intended for use in nonhostile environments up to 200 volts RMS or 300 volts DC.
- NOTES:

GRID R173H FOR PATTERN H AND P
VECTORBOARD 0.1 x 0.1 TO THE INCH.